EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	112	703/14.ccls. and @pd>"20080501"	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2008/10/09 10:28
L2	0	ioput and @pd>"20080501"	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2008/10/09 10:42
L3	5	(bidirectional adj node) and @pd>"20080501"	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2008/10/09 10:43
L4	5078	analog and bidirectional and mixed	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2008/10/09 10:58
L5	3948	analog and digital and bidirectional and mixed	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2008/10/09 10:58
L6	1104	analog and digital and bidirectional and ((mixed near signal) or mixed-signal)	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2008/10/09 10:59
L7	1104	analog and digital and bidirectional and ((mixed near signal) or mixed-signal)	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2008/10/09 11:05
L8	261	analog and digital and bidirectional and ((mixed near signal) or mixed-signal) and simulation	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2008/10/09 11:06

Google Scholar

ioput | 1990 | - | 2002 | Search   Ad Sc Sc

Scholar   All articles - Recent articles   Results **1 - 10** of about **143** for **ioput**. (0.36 seconds)

Did you mean: *input*

Efficient Learning of Non-Uniform B-Splines for Modelling and Control
J ZHANG, A KNOLL, I RENNERS - Computational Intelligence for Modelling, Control & ..., 1999 - books.google.com
... The universe of discourse of each **ioput** is divided into a numher of suhimervals, where each suhinterval is delimited hy hreakpoints called knots which ...
Cited by 5 - Related articles - Web Search - All 5 versions

[CITATION] AMOVA-PREP 1.01: A program for the preparation of AMOVA **ioput** Ides from dominant markers raw data [...
MP Miller - Computer software distributed by author, 1998
Cited by 1 - Web Search

Voltage level translation for an output driver system with a bias generator
F Hinedi, M Cases, S Dutta, RH Dennard - US Patent 5,986,472, 1999 - freepatentsonline.com
... A gate of transistor 118 is connected to bias circuit 130 at Node E. A drain of transistor 118 is connected to an information node **IOPUT**, which is connected to ...
Cited by 3 - Related articles - Web Search - All 3 versions

Stacked PFET off-chip driver with a latch bias generator for overvoltage protection
F Hinedi, L Mamileti - US Patent 6,141,200, 2000 - freepatentsonline.com
... Transistor P2 is connected at the drain to an output node connected, through resistor R, to output terminal **IOPUT**. Also connected ...
Related articles - Web Search - All 3 versions

Concurrent Hierarchical and Multilevel Simulation of VLSI Circuits
RB Mueller-Thuns, JT Rahmeh, JA Abraham, JA Wehbeh ... - SIMULATION, 1993 - sim.sagepub.com
Page 1. http://sim.sagepub.com SIMULATION DOI: 10.1177/003754979306000202
1993; 60; 79 SIMULATION Robert B. Mueller-Thuns, Joseph ...
Related articles - Web Search - BL Direct - All 2 versions

Multi-Agent-Systems Based on Coloured Petri Nets - ► uni-hamburg.de [PS]
D Moldt, F Wienberg - LECTURE NOTES IN COMPUTER SCIENCE, 1997 - Springer
... with the Object-ID. A place **ioput** liin0t I 176 obJ id* I method 1 I "I
I method 2 "i r ..... methodn t ..... Figure 3: Folding ...
Cited by 84 - Related articles - Web Search - BL Direct - All 7 versions

On the structure and the adjustment of the all-fiber isolator
Z Xiaopin, Y Peida - TENCON 90. 1990 IEEE Region 10 Conference on Computer and ..., 1990 - ieeexplore.ieee.org
... **Ioput** Polarizatlon (P!) CPCL) - polar:zcr I controller Fig.3 The SOP evolution for the tilt ... **Ioput** Polarizatlon (P!) CPCL) - polar:zcr I controller ...
Web Search

Evaluating the European Commission RTD Programmes: Responding to the
IKL O'SULLIVAN, GFL DURIEUX, Y DUMONT - Science Evaluation and Its Management, 1999 -

Google Scholar

| ioput analog | 1990 | - | 2002 | Search |

Ad
Sc
Sc

Scholar   All articles - Recent articles   Results **1 - 10** of about **18** for ioput analog. (0.26 seconds)

Did you mean: *input* analog

DONALD: a workbench for interactive design space exploration andsizing of analog circuits
K Swings, W Sansen - Design Automation. EDAC. Proceedings of the European ..., 1991 - ieeexplore.ieee.org
... circuit sizing. The program is de to numerically invert an analytic design
model of **analog** circuit in any arbitrary direction. It ...
Cited by 23 - Related articles - Web Search - All 4 versions

Design methodology and simulation tools for mixed analog-digitalintegrated circuits
R Beale, R Chadha, CF Chen, A Prosser, KM Tham - Circuits and Systems, 1990., IEEE International
Symposium on, 1990 - ieeexplore.ieee.org
... floating capacitances form a group. A behavioral **analog** model is added to the adjoining
group if it is connected at the **ioput** terminals with that group. ...
Cited by 1 - Related articles - Web Search

... Analogic Cellular Optical Computer Using Bacteriorhodopsin as Analog Rewriteable Image Memory
S TOKES, LOG VARO, DER Andras, P ORMOS - Bioelectronic Applications of Photochromic Pigments, 2001 -
books.google.com
... While several emulated digital and mixed- signal **analog** implementations are emerging ...
The large neighborhood templates canfind complex Imoges on the **ioput** Imoge. ...
Cited by 2 - Related articles - Web Search - BL Direct

Application of chaos theory to digital transducer design
PA Littlehales, CP Lewis, SR Bishop - Control, 1994. Control'94. Volume 2., International ..., 1994 -
ieeexplore.ieee.org
... It may be used as an **analogue**. ... Conventional devices produce an **analogue** signal which
has then to be converted to a digital fomat for piocessing. ...
Web Search - BL Direct - All 3 versions

An integrated mixed-mode neural network architecture formegasynapse ANNs
J Schemmel, F Shurmann, S Hohmann, K Meier - Neural Networks, 2002. IJCNN'02. Proceedings of the 2002 ...,
2002 - ieeexplore.ieee.org
... art approach in between these two opposite poles by combining fully **analog** network
blocks ... the output at time t depends not oniy on the actual **ioput**, but also ...
Related articles - Web Search

M3-A MultilevelMixed-Mode Mixed A/D Simulator - ▶ acm.org [PDF]
R Chadha, C Visweswariah, CF Chen - IEEE Trans. Computer-Aided Design, 1992 - ieeexplore.ieee.org
... group. A be- havioral **analog** model is added to the adjoining group if it
is connected at the **ioput** terminals with that group. Thus ...
Cited by 8 - Related articles - Web Search - All 6 versions

NETHDL: abstraction of schematics to high-level HDL
D Fischer, Y Levhari, G Singer - Proceedings of the conference on European design automation, 1990 -
portal.acm.org
... Conventional circuit (**analog**) simulators (SPICE [1], ASTAP [2]) are not suited

Web   Images   Video   News   Maps   **more »**

| bidirectional analog digital | 1990 | - | 2002 | Search | Ad Sc Sc |

Scholar   All articles - Recent articles Results **1** - **10** of about **19,000** for bidirectional analog digital.

256-channel **bidirectional** optical interconnect using VCSELs and photodiodes on CMOS
DV Plant, MB Venditti, E Laprise, J Faucher, K … - Journal of Lightwave Technology, 2001 - OSA
… of a 2-D **bi-directional** free-space … PLANT et al.: 256-CHANNEL **BIDIRECTIONAL** OPTICAL
INTERCONNECT … include optoelectronic devices, mixed **analog/digital** ASIC design …
Cited by 81 - Related articles - Web Search - BL Direct - All 10 versions

A single-fiber **bidirectional** optical link using colocated emittersand detectors
J Cross, A Buchanan, B Carastro, LSCW Jokerst, NH … - Photonics Technology Letters, IEEE, 1996 -
ieeexplore.ieee.org
… These colocated devices enable single fiber **bidirectional** communication between
the two **analog/digital** silicon circuits that contain the emitteddetector pairs. …
Cited by 15 - Related articles - Web Search - BL Direct - All 3 versions

HIGH SPEED **BIDIRECTIONAL DIGITAL** CABLE TRANSMISSION SYSTEM
TM Paff, H Graham - EP Patent 0,754,378, 2002 - freepatentsonline.com
… cable system into more versatile **bidirectional** communication systems … is in the form
of **analog** television signals … now have the ability to send **digital** commands to …
Cited by 20 - Related articles - Web Search - All 7 versions

Superconducting **analog-to-digital** converter with grounded four-junction squid **bidirectional** counter
GS Lee - 1991 - freepatentsonline.com
… 4646060, February, 1987, Phillips et al. 340/347AD, Superconducting
analog-to-digital converter with **bidirectional** counter. 4672359, …
Cited by 11 - Related articles - Web Search - All 6 versions

Bidirectional digital serial interface for communication **digital** signals including digitized audio …
KA Kasparian, AS Rogers, MC Hsu, JP Fussell - US Patent 5,007,050, 1991 - freepatentsonline.com
… The Interface System is duplex (**bidirectional**). … between the Control Unit and the
Transceiver Unit, having separate **analog** audio, **digital** data and control …
Cited by 26 - Related articles - Web Search - All 3 versions

Cellular telephone for monitoring **analog** and **digital** control channels
JEAS Dahlin, WGA Mueller, W Ghisler - US Patent 5,420,911, 1995 - freepatentsonline.com
… The **bi-directional** multi-mode mobile station can use either **analog** or **digital**
traffic channels and **analog** or **digital** control channels. …
Cited by 27 - Related articles - Web Search - All 3 versions

Weight Perturbation: An Optimal Architecture and Learning Technique for **Analog** VLSI Feedforward and …
M Jabri, B Flower - Neural Computation, 1991 - MIT Press
… is shown in Figure 1. For fully parallel **analog** implementations of … **Bi-Directional** …
flow for backpropagation training when fully **bidirectional** information paths …
Cited by 148 - Related articles - Web Search - All 10 versions

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Scholar All articles - Recent articles Results **1 - 10** of about **4,210** for bidirectional analog digital s

256-channel bidirectional optical interconnect using VCSELs and photodiodes on CMOS

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… of a 2-D bi-directional free-space … PLANT et al.: 256-CHANNEL BIDIRECTIONAL OPTICAL INTERCONNECT … include optoelectronic devices, mixed analog/digital ASIC design …

Cited by 81 - Related articles - Web Search - BL Direct - All 10 versions

Power play-fast dynamic power estimation based on logic simulation

TH Krodel, BN Res, O Ottawa - Computer Design: VLSI in Computers and Processors, 1991. …, 1991 - ieeexplore.ieee.org

… not cause the transition on the bidirectional line … Since the analog simulator has a precise knowledge of … times: Powerplay: 0.42 sec digital simulation: 0.24 sec …

Cited by 30 - Related articles - Web Search - All 3 versions

[BOOK] Mixed-Mode Simulation and Analog Multilevel Simulation

RA Saleh, SJ Jou, AR Newton - 1994 - books.google.com

… systems in the time-domain. A complete hierarchy of modeling and simulation methods for analog and digital circuits is described. Fig. …

Cited by 45 - Related articles - Web Search - Library Search - All 2 versions

Method for capturing ASIC I/O pin data for tester compatibility analysis

SD Johnson - US Patent 5,995,740, 1999 - freepatentsonline.com

… operation of a circuit having analog and digital … 395/275, Bi-directional buffers for mounting a … 395/500.34, Method for modeling bidirectional or multiplicatively …

Cited by 14 - Related articles - Web Search - All 3 versions

Verilog-AMS: Mixed-signal simulation and cross domain connect modules - ▶ columbia.edu

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P Frey, DO'Riordan - … International Workshop on Behavioral Modeling and Simulation, 2000 - doi.ieeecs.org

… as analog-to-digital converters, digital-to-analog converters, or bidirectional converters, cause … Rule 9 Any digital nets that are connected to the …

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Evaluating mixed-signal simulators

D Overhauser, R Saleh - Custom Integrated Circuits Conference, 1995., Proceedings of …, 1995 - ieeexplore.ieee.org

… Section I11 discusses issues in- volved with a unidirectional analog-to-digital interface, Section IV discusses bidirectional interfacing issues …

Cited by 10 - Related articles - Web Search - All 2 versions

[PDF] ▶ Evolution of continuous-time modeling and simulation

KJ Åström, H Elmqvist, SE Mattsson - … the 12th European Simulation Multiconference on Simulation- …, 1998 - diquima.upm.es

… Analog techniques were predominant from 1920 to 1950. Major changes took place when digital computers were available and simulation techniques have then …

Cited by 54 - Related articles - View as HTML - Web Search - All 8 versions

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RA Saleh, SJ Jou, AR Newton - 1994 - books.google.com
... Page 22. **MIXED**-MODE **SIMULATION** This book addresses the problems of simulating
entire **mixed analog/digital** systems in the time-domain. ...
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Verilog-AMS: Mixed-signal simulation and cross domain connect modules - ▶ columbia.edu
[PDF]
P Frey, DO'Riordan - ... International Workshop on Behavioral Modeling and **Simulation**, 2000 - doi.ieeecs.org
... as **analog**-to-**digital** converters, **digital**-to-**analog** converters, or **bidirectional**
converters, cause ... Rule 9 Any **digital** nets that are connected to the ...
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Evaluating mixed-signal simulators
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... Section I11 discusses issues in- volved with a unidirectional **analog**-to-**digital**
interface, Section IV discusses **bidirectional** interfacing issues ...
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[BOOK] **Applications of Vhdl to Circuit Design**
RE Harr, AG Stanculescu - 1991 - books.google.com
... gate as a network of **bi-directional** Switches 36 ... for UART Design 154 5.26 **Bidirectional**
Modeling (for ... Describing the functionality of **analog** circuits including ...
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Method for capturing ASIC I/O pin data for tester compatibility analysis
SD Johnson - US Patent 5,995,740, 1999 - freepatentsonline.com
... operation of a circuit having **analog** and **digital** ... 395/275, **Bi-directional** buffers
for mounting a ... 395/500.34, Method for modeling **bidirectional** or multiplicatively ...
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MB Srivastava, RW Brodersen - Design & Test of Computers, IEEE, 1992 - ieeexplore.ieee.org
... aided software engineering, and **digital** signal processing ... Electromechanical interfaces
Analog power electronics Robot ... flow unidirection- al or **bidirectional**? ...
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Event-driven behavioural simulation of analogue transfer functions
RA Cottrell - Design Automation Conference, 1990. EDAC. Proceedings of the ..., 1990 - ieeexplore.ieee.org
... input, provided the **simulator** could support such **bidirectional** transfer of ... much the
same way that behavioural **simulation** is used in **digital** circuit design ...
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A backplane approach for cosimulation in high-level systemspecification environments
S Schmerler, Y Tanurhan, KD Muller-Glaser - Design Automation Conference, 1995, with EURO-VHDL, ..., 1995
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